ABSRACT

A circuit arrangement is provided which has the advantage of limiting the magnitude of the current to a transimpedance amplifier (TIA) circuit or a current to voltage converter during an overdrive condition, thereby preventing an amplifier of the TIA circuit from non-linear operation and from outputting high output currents. The circuit arrangement further prevents the generation of thermal tails which may cause the loss of data in memory circuits. The circuit arrangement limits the magnitude of the current to the TIA circuit during the overdrive condition by utilizing a current limiter circuit having a Schottky bridge. The Schottky bridge provides an open circuit arrangement between the TIA circuit and a current source when the overdrive condition occurs.